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DEC 08 2003  
PATENT & TRADEMARK OFFICE

WHINNETT ET AL.  
CE11860EP (SERIAL NO. 10/644,544)

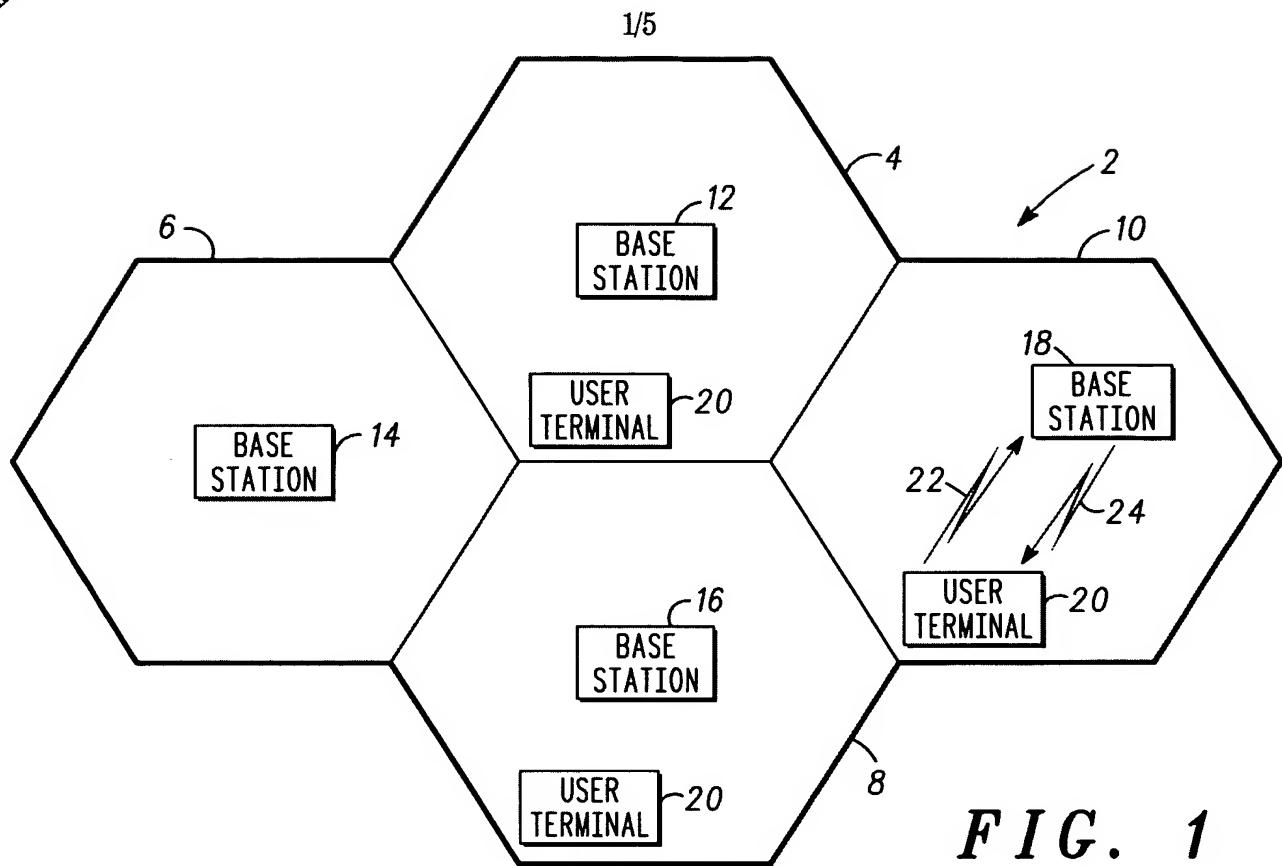


FIG. 1

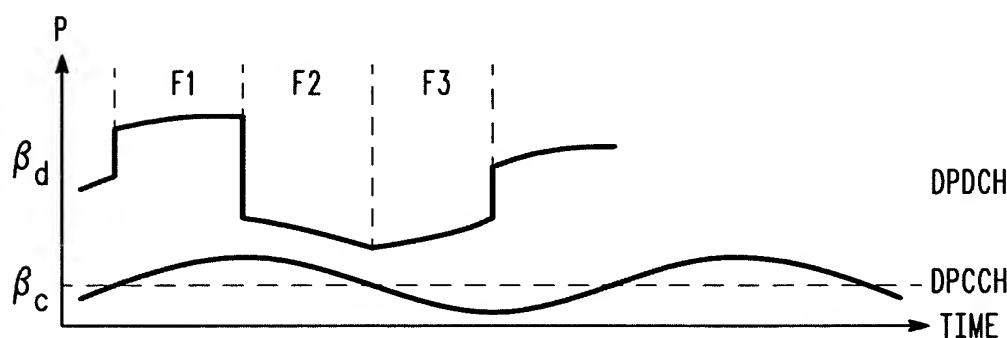


FIG. 2

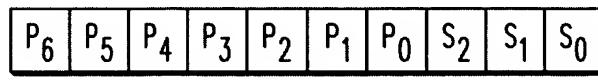


FIG. 3

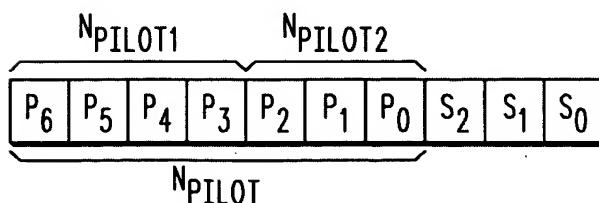


FIG. 4



SLOT FORMAT NUMBER	CHANNEL BIT RATE (KBPS)	CHANNEL SYMBOL RATE (KSPS)	SC	BITS/ FRAME	BITS/ SLOT	N <sub>PILOT1</sub>	N <sub>PILOT2</sub>	N <sub>TPC</sub>	N <sub>TFCI</sub>	N <sub>FBI</sub>	TRANSMITTED SLOTS PER RADIO FRAME
0	15	15	256	150	10	6	0	2	2	0	15
0A	15	15	256	150	10	5	0	2	3	0	10-14
0B	15	15	256	150	10	4	0	2	4	0	8-9
1	15	15	256	150	10	8	0	2	0	0	8-15
2	15	15	256	150	10	5	0	2	2	1	15
2A	15	15	256	150	10	4	0	2	3	1	10-14
2B	15	15	256	150	10	3	0	2	4	1	8-9
3	15	15	256	150	10	7	0	2	0	1	8-15
4	15	15	256	150	10	6	0	2	0	2	8-15
5	15	15	256	150	10	5	0	1	2	2	15
5A	15	15	256	150	10	4	0	1	3	2	10-14
5B	15	15	256	150	10	3	0	1	4	2	8-9
6	15	15	256	150	10	4	3	1	2	0	15

FIG. 5



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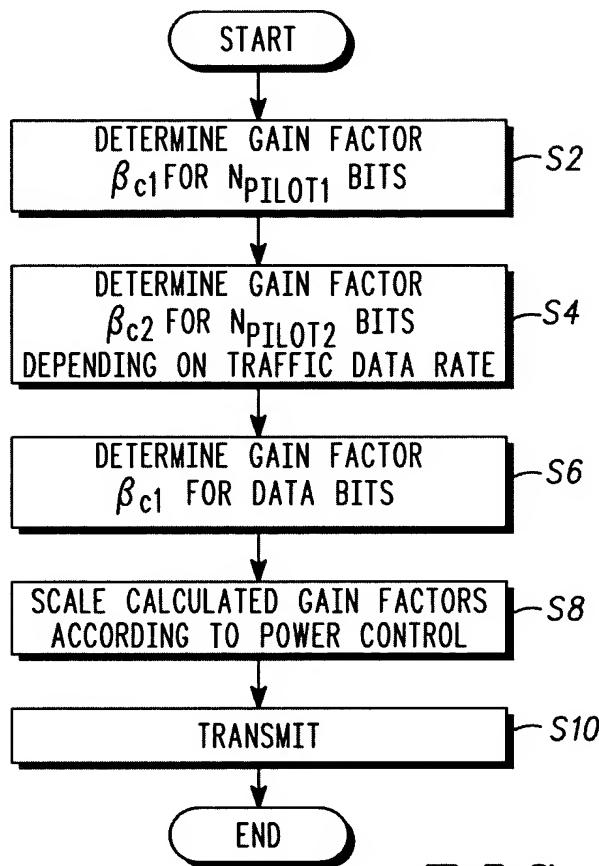


FIG. 6

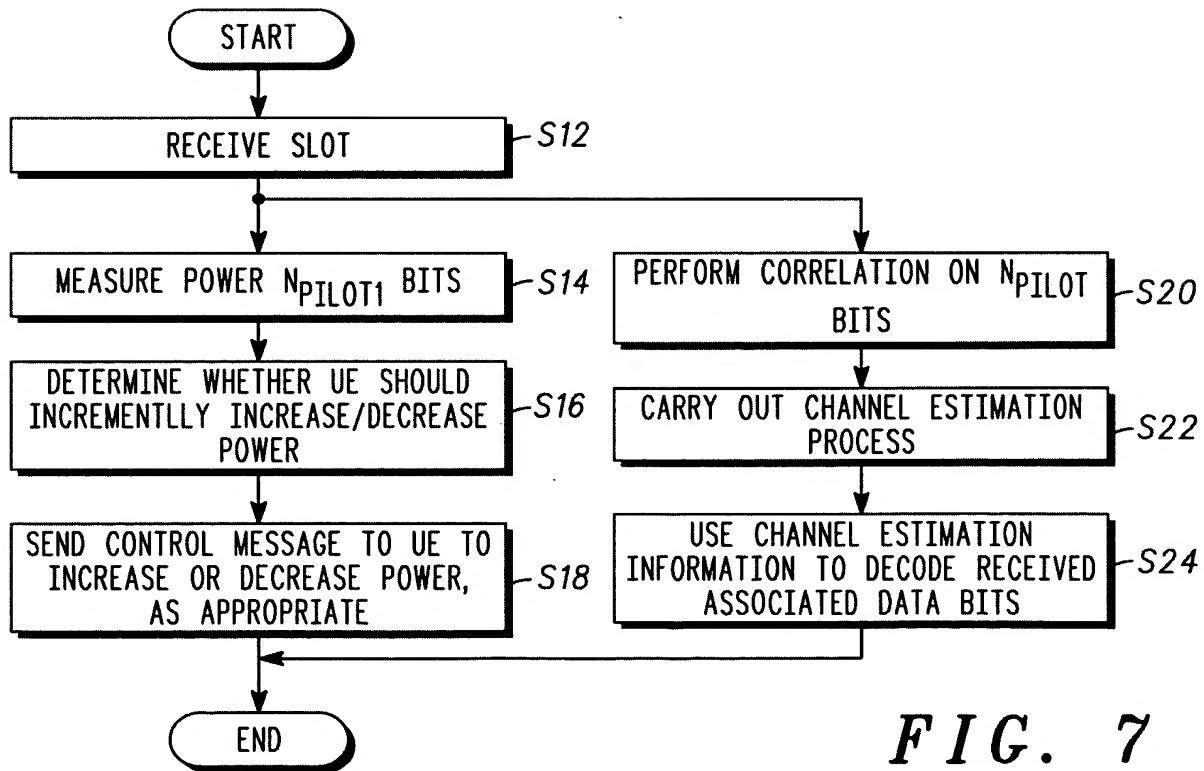
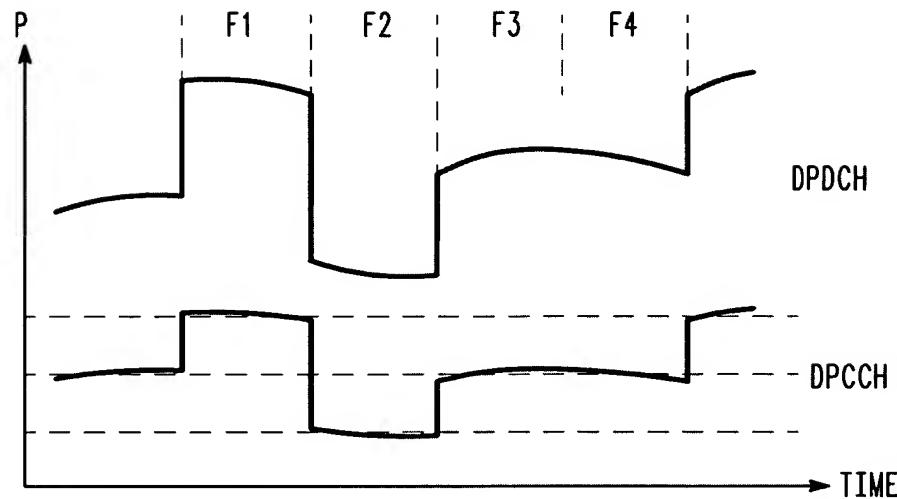
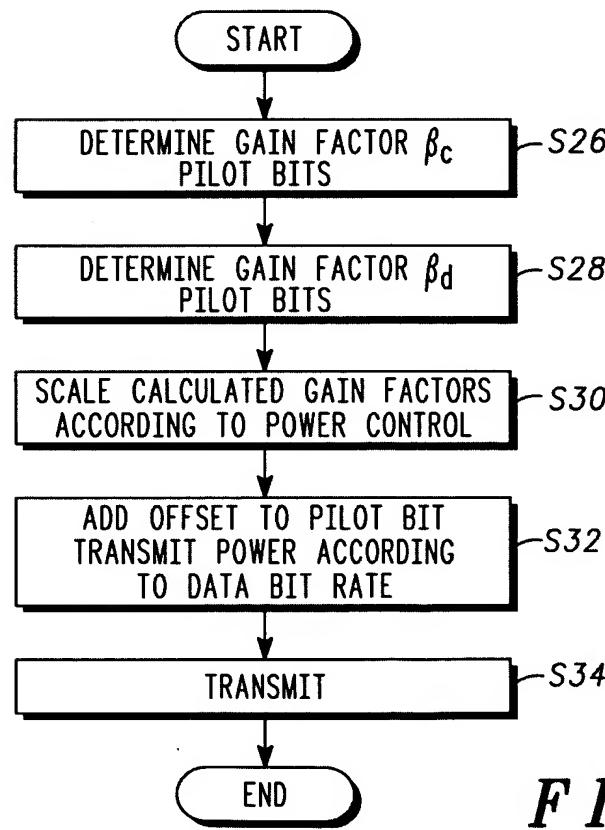


FIG. 7



**FIG. 8**



**FIG. 9**

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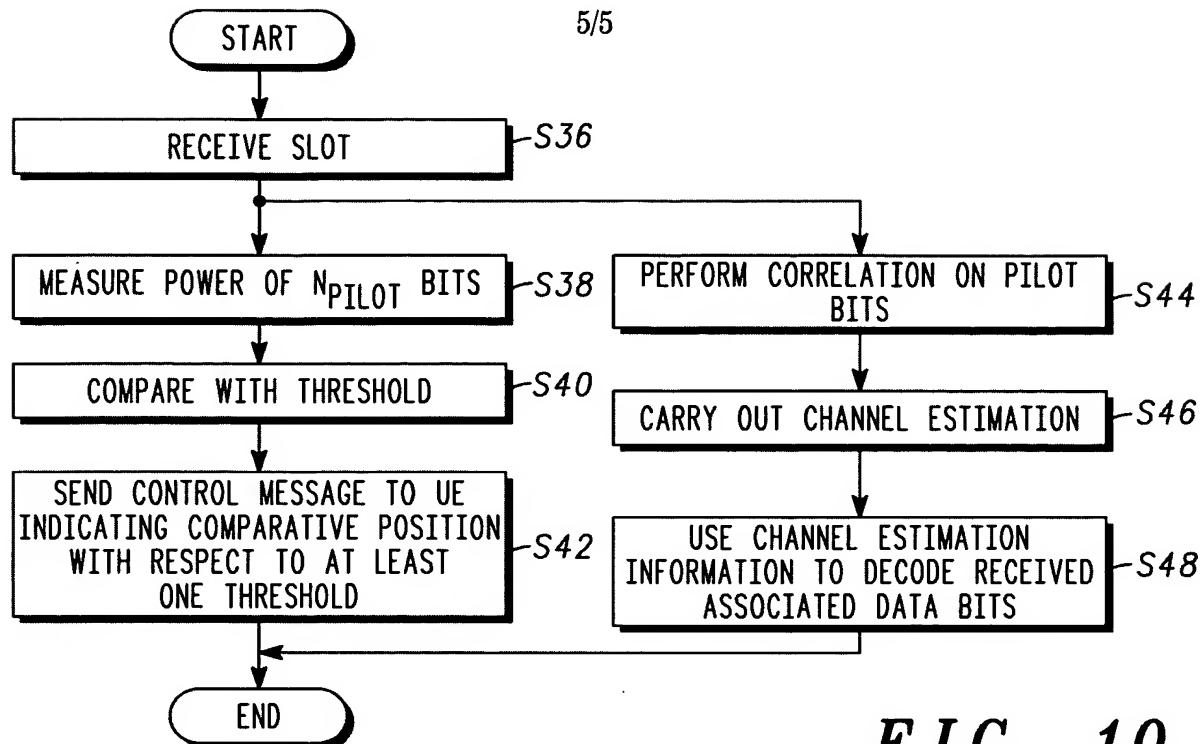


FIG. 10

POWER CONTROL METRIC SIR <sub>MEASURED</sub>	POWER CONTROL COMMANDS
THRESHOLD <sub>INNER_LOOP_3</sub> < SIR <sub>MEASURED</sub>	00
THRESHOLD <sub>INNER_LOOP_2</sub> < SIR <sub>MEASURED</sub> ≤ THRESHOLD <sub>OUTER_LOOP-3</sub>	01
THRESHOLD <sub>INNER_LOOP_1</sub> < SIR <sub>MEASURED</sub> ≤ THRESHOLD <sub>OUTER_LOOP-2</sub>	11
SIR <sub>MEASURED</sub> ≤ THRESHOLD <sub>INNER_LOOP_1</sub>	10

FIG. 11

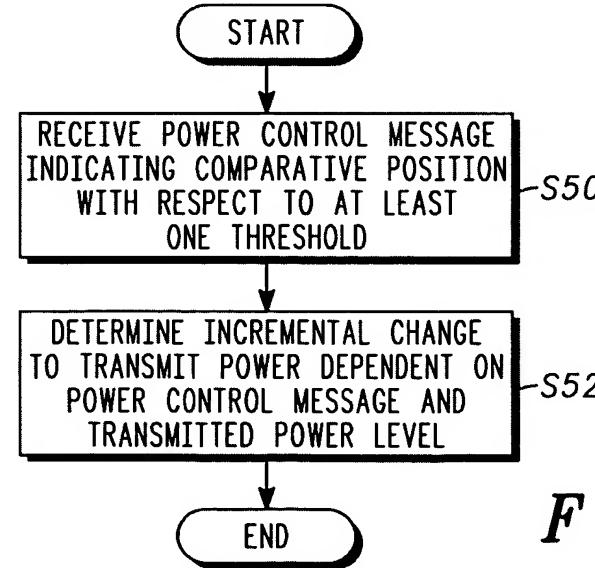


FIG. 12